

**José L. Ausín** received the B.Sc. degree in Electronic Physics from the University of Cantabria, Santander, Spain, and the Ph.D. degree in Electronic Physics from the University of Extremadura, Badajoz, Spain, in 1993 and 2000, respectively.

In 1995, on a research scholarship from the Spanish R&D Plan, he joined Department of Electronics and Electrical Engineering, University of Extremadura, here he was involved in sampled data circuits design. In the fall of 2004 and the spring of 2005 he was a Visiting Scholar at the Electrical Engineering Department of Texas A&M University, College Station, on a Spanish Science Ministry Fellowship. Currently, he is with the University of Extremadura, where he is an Associate Professor. His research interest focuses mainly on the area of CMOS analog and mixed-signal circuit design for signal processing.