

## Articulos en memorias de congresos recientes: 2018, 2017

### (Full papers in proceedings):2018, 2017

- G. Jovanovic Dolecek ,”**Modified Two-Stage Cosine-Based Decimator**Improving comb alias rejection using Modified cosine filters,” Accepted for *ISCAS 2018*, May 2018, Florence, Italy.
- G. Jovanovic Dolecek, R. G. Baez, M. Laddomada, “**Design of Efficient Multiplierless Modified Cosine-Based Comb Decimation Filters: Analysis and Implementation**,” Accepted for *ISCAS 2018*, May 2018, Florence, Italy.
- G. Jovanovic Dolecek and R. Flores Rodrigues, “**Novel Two-Stage Comb Filter for Multiple-of-Three Decimation Factors**,” Accepted for *LASCAS 2018*, February, 2018, Puerto Vallarta, Mexico.
- G. Jovanovic Dolecek, “**Improving Comb Alias Rejection by Exploiting Certain Class of Palindromic Polynomials**,” *2017 European Conference on Circuit Theory and Design (ECCTD)*, Catania, Italy, 4-6 Sept. 2017, pp.1-4. Proc. published in IEEEXplore, Electronic ISSN: 2474-9672, DOI: [10.1109/ECCTD.2017.8093347](https://doi.org/10.1109/ECCTD.2017.8093347) .
- G. Jovanovic Dolecek, “**Two-Stage Cosine Filter-Based Decimator with Improved Aliasing Rejection**,” *2017 New Generation of Circuits and Systems (NGCAS)*, Genova, Italy, 06-09 September 2017.pp.257-260, Proc published in IEEEXplore., Electronic ISSN: 978-1-5090-6447-2/17, DOI 10.1109/NGCAS.2017.77.
- G. Jovanovic Dolecek, “**Simple method to improve comb frequency characteristic in passband and folding bands**,” Proc of *ICMSQUARE 2017*, Pafos, Cyprus, pp.1-7. The paper published in J. Phys.: Conf. Ser. **936** 012027.
- G. Jovanovic Dolecek, “**Multiplierless Wideband and Narrowband CIC Compensator for SDR Application**,” *13th International Conference on Wireless Communication, Networking and Mobile Computing (WiCOM 2017)*, Guilin, China from August 21-23, 2017. Paper published in Int journal on Communications, Network and System Science, pp. 1-6.
- G. Jovanovic Dolecek and J. Carmona Suarez, “**Improving Alias Rejection in Comb Decimation Filters for Odd Decimation Factors**”,Proc. *MWSCAS 2017*, Boston, MA,pp.1-4 . Proc published in IEEEXplore. Electronic ISSN: 1558-3899, DOI: [10.1109/MWSCAS.2017.8052944](https://doi.org/10.1109/MWSCAS.2017.8052944)
- G. Jovanovic Dolecek and L. H. Sepulveda,” **Improving Magnitude Response in Two-Stage Corrector Comb Structure**,”Proc. *2017 International Caribbean Conference on Devices, Circuits and Systems (ICCDCS)*, Cozumel, Mexico, 5-7 June 2017, pp.73-76. Proc published in IEEEXplore, Electronic ISSN: 2165-3550, DOI: [10.1109/ICCDCS.2017.7959701](https://doi.org/10.1109/ICCDCS.2017.7959701)
- G. Jovanovic Dolecek and R. Garcia Baez, “**Nonrecursive Comb-Based Structure for Power of Three Decimation Factors: Design and FPGA Implementation**,” Proc. *IEEE Latin American Symp of Circuits and Systems, LASCAS*, Bariloche Argentina, 20-23 Feb, 2017, pp.1-4, Proc published in IEEEXplore, Electronic ISSN: 2473-4667, DOI: [10.1109/LASCAS.2017.7948069](https://doi.org/10.1109/LASCAS.2017.7948069)
- G. Jovanovic Dolecek, “**Modified Nonrecursive Comb Decimation Structure**,” Proc. *2017 IEEE International Conference on Consumer Electronics (ICCE)*, Las Vegas, Nevada, USA, 8-10 Jan. 2017. Pp1-2, Proc published in IEEEXplore, Electronic ISSN: 2158-4001, DOI: [10.1109/ICCE.2017.7889209](https://doi.org/10.1109/ICCE.2017.7889209)