

Kenneth W. Martin was a Professor at UCLA from 1980 to 1991. He attained tenure (Associate Professor) in 1982, and became a Full Professor in 1987. In 1985, he founded the Integrated Circuits and Systems Laboratory (ICSL) and Major Field at UCLA, which became the incubator of many high-tech companies in Southern California, including Broadcom. In 1991, Professor Martin returned to the University of Toronto to accept a position as an Endowed Professor, which he held until 2008, when he became an Adjunct Professor. While at the University of Toronto, he co-authored (with David Johns) *Analog Integrated Circuit Design*, a popular graduate-level textbook. A second edition was published in 2011, with Prof. Tony Chan-Carusone joining as an additional co-author. He has also co-authored numerous other books, chapters, and well over 100 papers.

Professor Martin has received many awards: He was selected by the IEEE Circuits and Systems Society for the Outstanding Young Engineer Award, which was presented at the IEEE Centennial "Keys to the Future" Program in 1984. Dr. Martin was granted the NSF Presidential Young Investigator's Award, which continued from 1985 to 1990. He was a co-recipient of the Beatrice Winner Award at the 1993 ISSCC and a co-recipient of the 1999 IEEE Darlington Best-Paper Award for the paper "Transactions on the Circuits and Systems." He was also awarded the 1999 CAS Golden Jubilee Medal of the IEEE Circuits and Systems Society. Professor Martin is a *Fellow of the IEEE* (FIEEE).

Dr. Martin founded (along with David Johns) Snowbush Microelectronics in 1988; the enterprise grew organically from one employee to a highly profitable company with 50 employees, including a Mexican Design Center (ran by Jorge Varona and now at over 30 employees), in its ten year existence. Snowbush was acquired by Gennum Corp. in 2007, and is still highly successful.

Professor Martin is currently President of his newest venture, Granite SemiCom Inc., a company focused on analog/digital IP intended for higher frequencies and sub-micron technologies, where extensive use of digital calibration and customization is heavily used to enhance robustness and portability.