

# TEMARIO DEL CURSO DE FUNDAMENTOS DE DISPOSITIVOS ELECTRONICOS

## 1. Introducción a Física Electrónica

1.1 Propiedades de cristales y crecimiento de semiconductores

1.2 Átomos y electrones

**1.3 Bandas de energía y portadores de carga en semiconductores**

1.4 Exceso de portadores en semiconductores

## 2. Uniones

2.1 Fabricación de uniones p-n

2.2 Condiciones de equilibrio

2.3 Polarización de uniones en directa e inversa bajo condiciones de estado estacionario

2.4 Ruptura bajo polarización inversa

2.5 Condiciones de transitorio y a-c

2.6 Desviaciones de la teoría sencilla

2.7 Uniones metal-semiconductor

This section discusses the *specific mechanisms by which current flows in a solid*, thus laying the basis to understand why some materials are good conductors of electric current and others not.

## Shell model of the atom

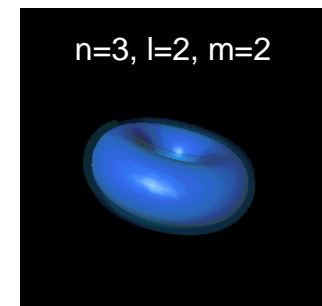
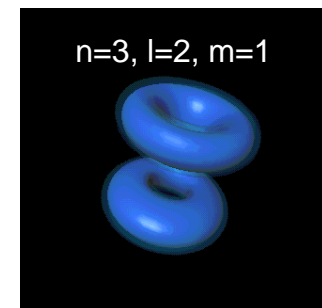
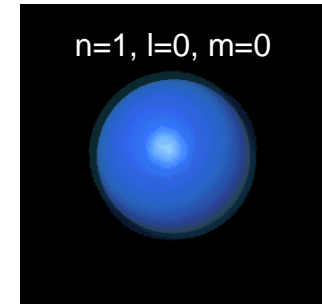
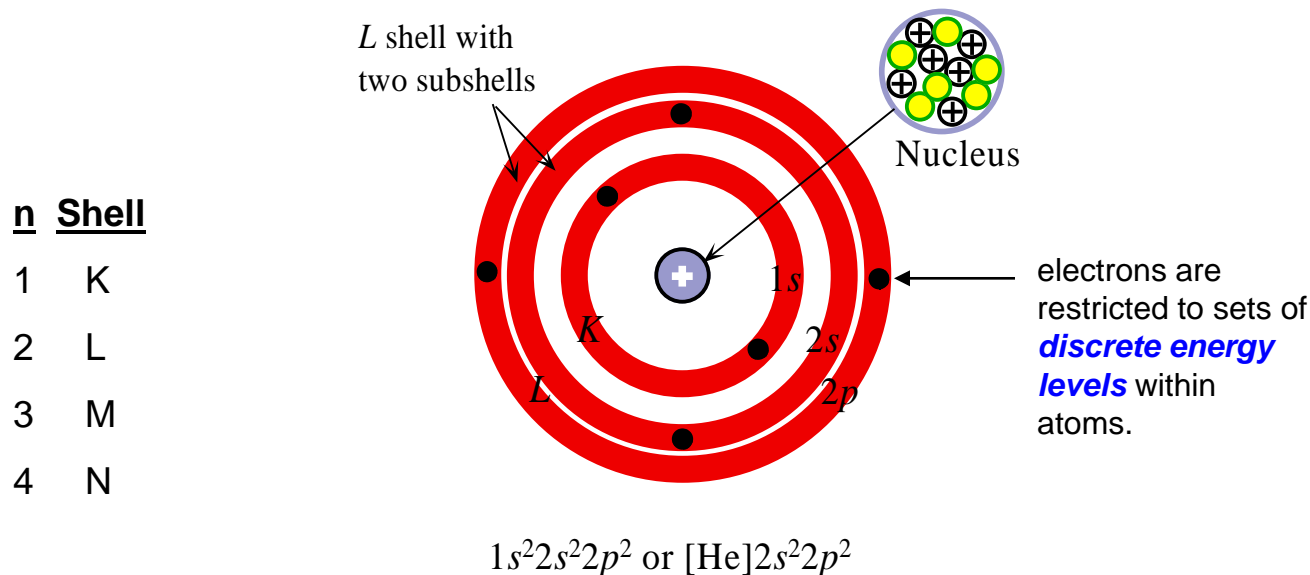


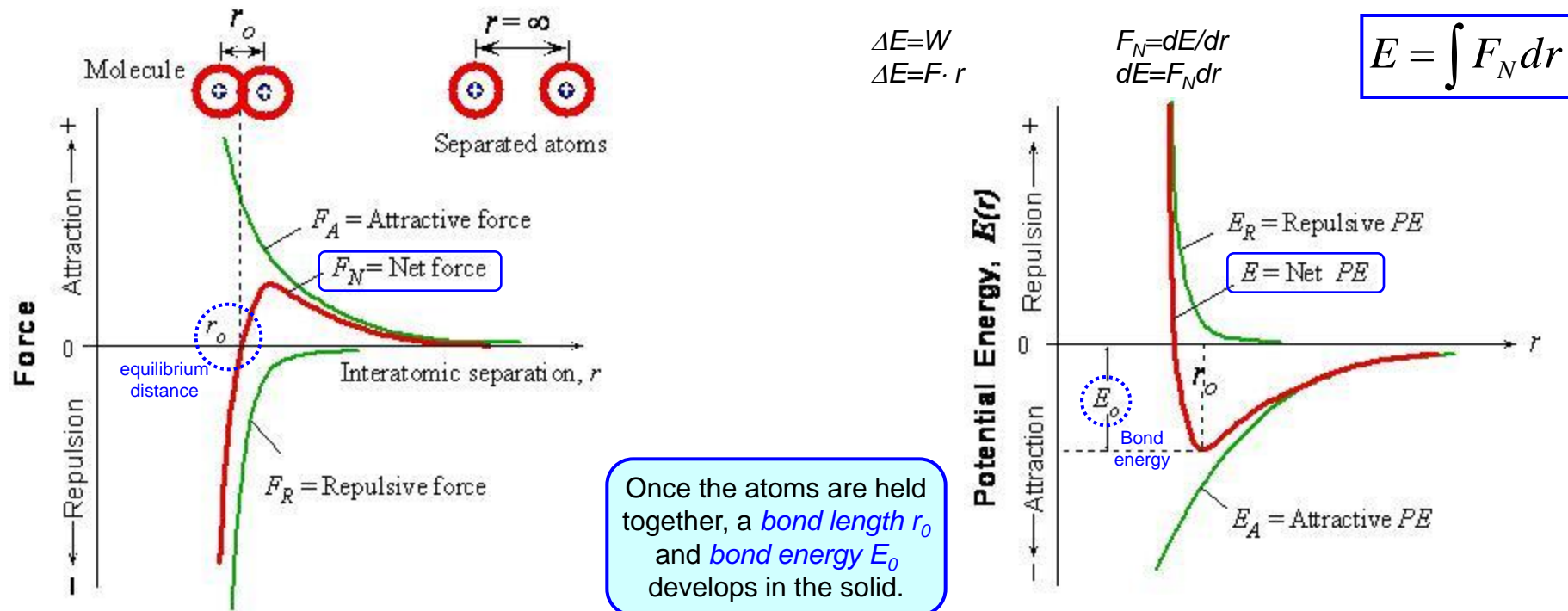
Fig. 1.1: The shell model of the atom in which the electrons are confined to live within certain shells and in subshells within shells.

From *Principles of Electronic Materials and Devices, Second Edition*, S.O. Kasap (© McGraw-Hill, 2002)  
<http://Materials.USask.Ca>

# Bonding Forces and Energy Bands in Solids

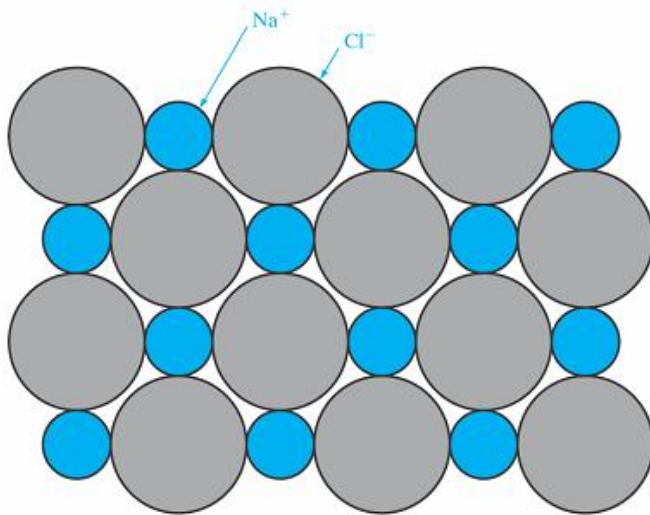
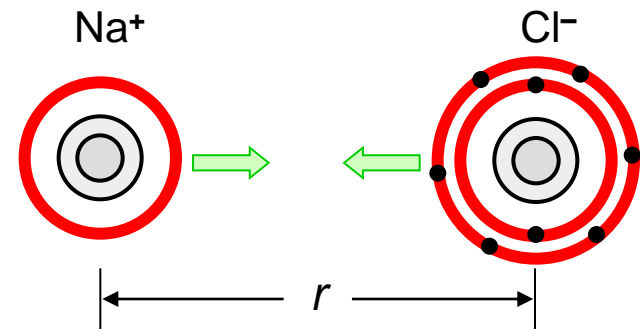
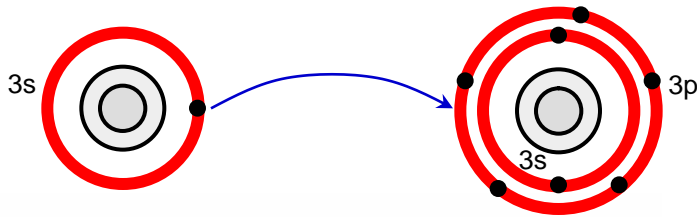
The influence of neighboring atoms on the energy levels of a particular atom gives rise to shifting and splitting of energy states into energy bands. This interaction of electrons in neighboring atoms of a solid serves to hold the crystal together.

When 2 atoms are brought together, the valence electrons interact with each other (*repulsive forces  $F_R$* ) and with the neighbor's positively charged nucleus (*attractive forces  $F_A$* ). The result of this interaction is often the formation of a bond between the 2 atoms, producing a molecule.

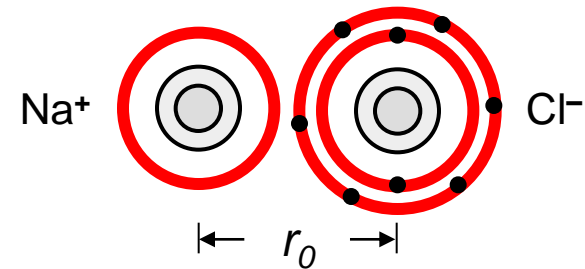
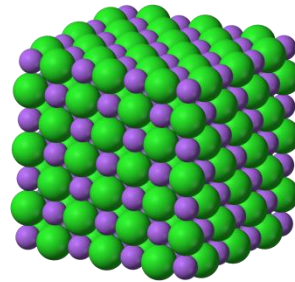


# Ionically Bonded Solids

Ionic bonding is frequently found in materials that normally have a metal and a nonmetal as the constituent elements. Transfer of valence electrons between the 2 elements results in 2 oppositely charged ions called cation and anion.



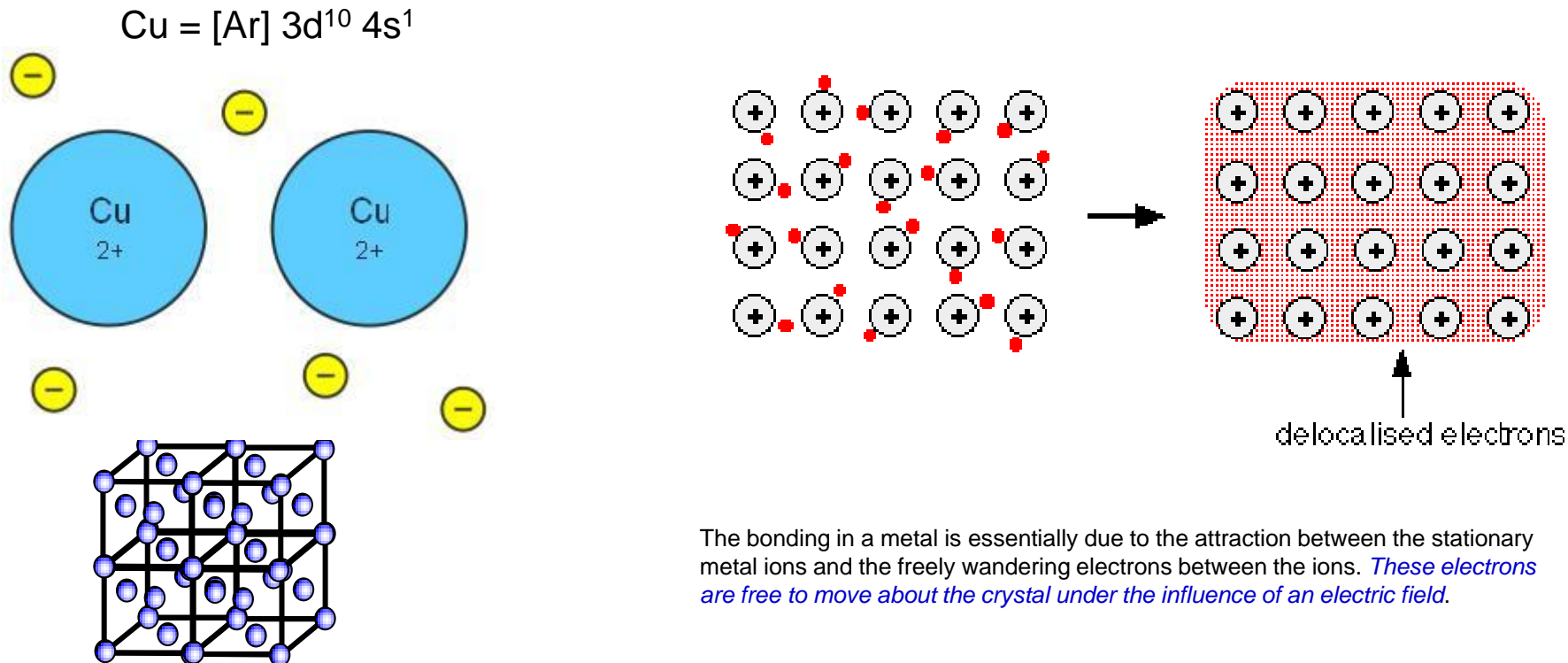
(a)



Once the electron exchanges have been made, the outer orbits of all atoms are completely filled and there are no bound electrons to participate in current flow.

# Metallically Bonded Solids

Metal atoms have only a few valence electrons, which are not very difficult to remove. When many metal atoms are brought together to form a solid, these valence electrons are lost from individual atoms and become collectively shared by all the ions. **The valence electrons become delocalized and form an electron gas or electron cloud, permeating the space between the ions.**

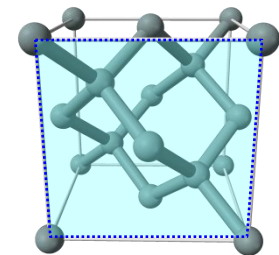
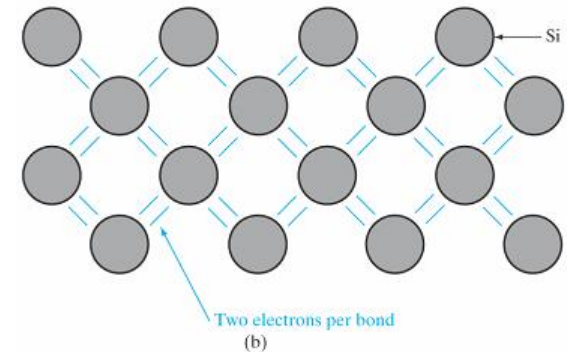
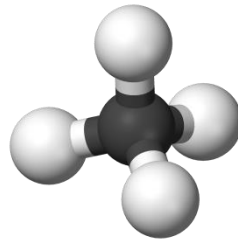
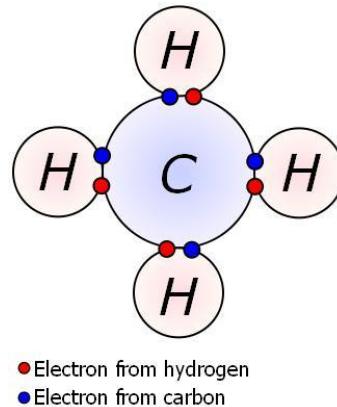
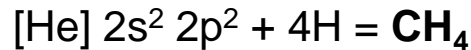
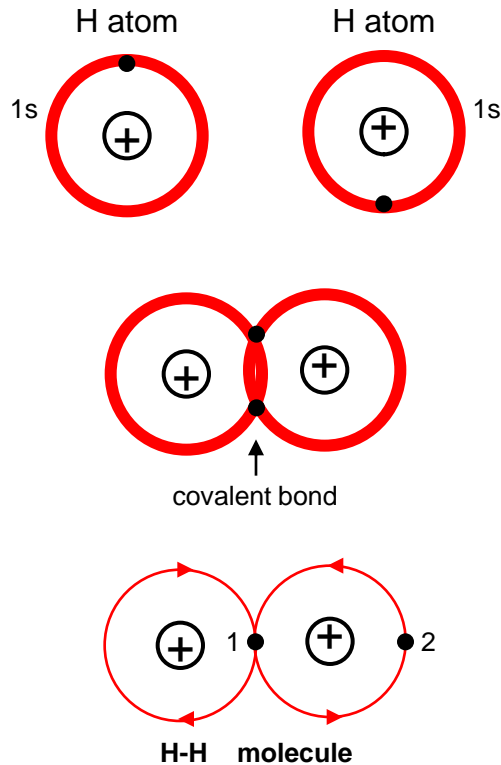


# Covalently Bonded Solids

2 atoms can form a bond with each other by sharing some or all of their valence electrons and thereby reducing the overall potential energy of the combination.

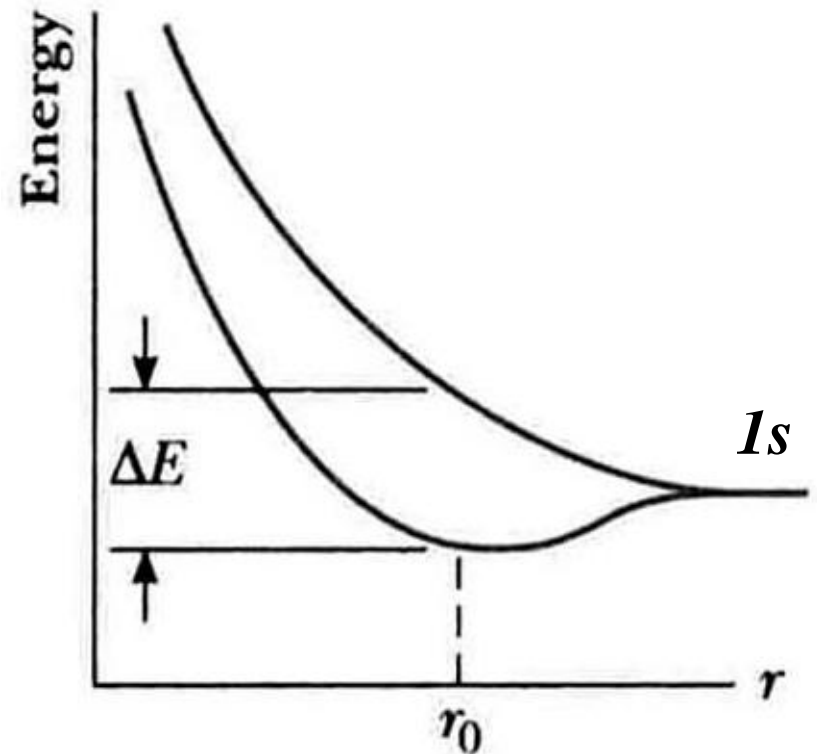
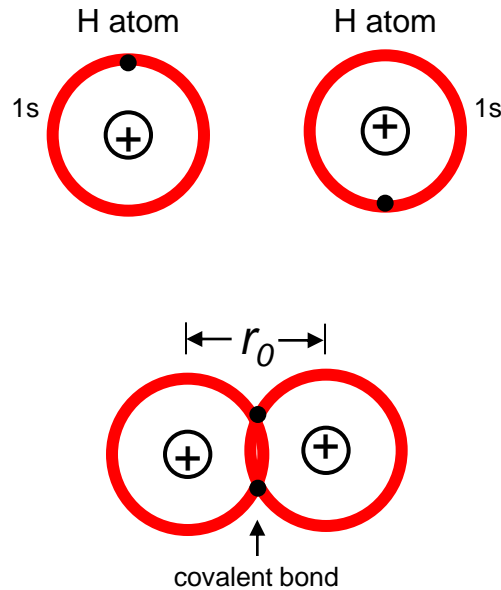
The covalent bond results from the sharing of valence electrons to complete the subshells (saturate the valence) of each atom.

The covalent bond can also be found for more complex structures like CH<sub>4</sub>, C, Si, etc. The covalent bond energy is usually the highest for all bond types: high T<sub>m</sub>, hardness, etc.



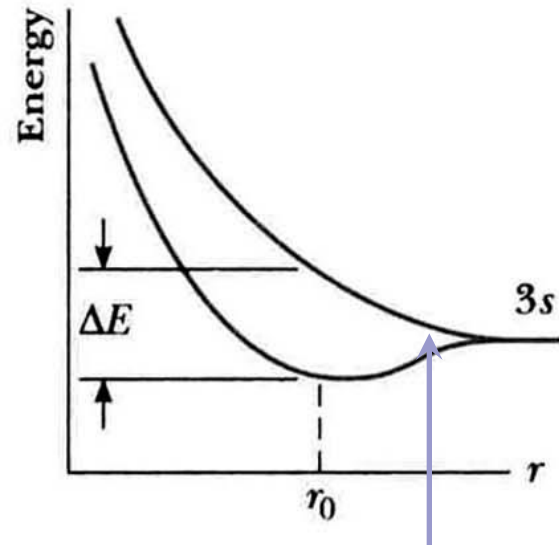
# Energy Bands

As isolated atoms are brought together to form a solid, various interactions occur between neighboring atoms. The forces of attraction and repulsion between atoms will find a balance at the proper interatomic spacing of the crystal. In the process, *important changes occur in the electron energy level configurations, which result in the varied electrical properties of solids.*

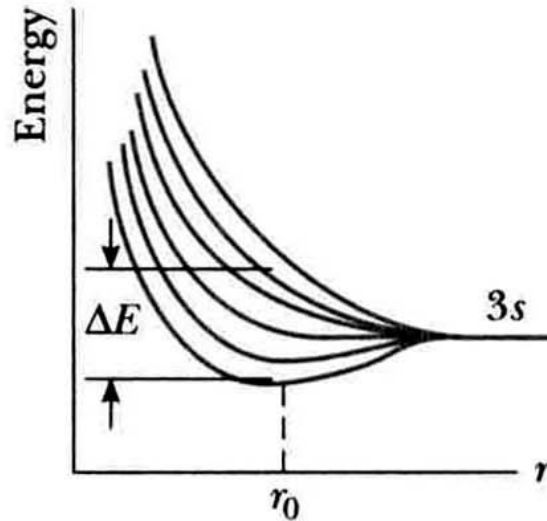




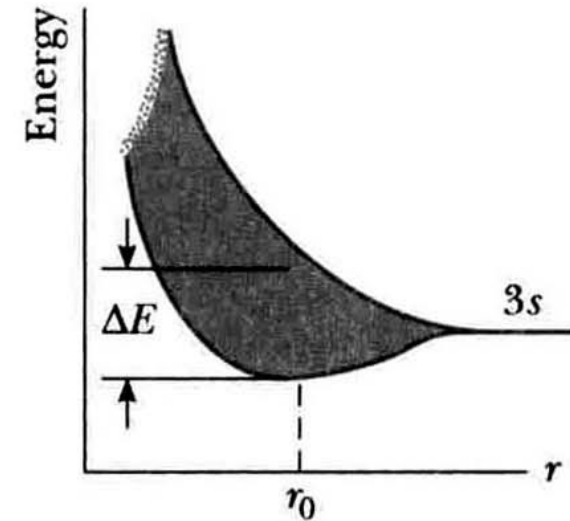
## Two atoms



## Six atoms



## Solid of $1 \times 10^{22}$ atoms



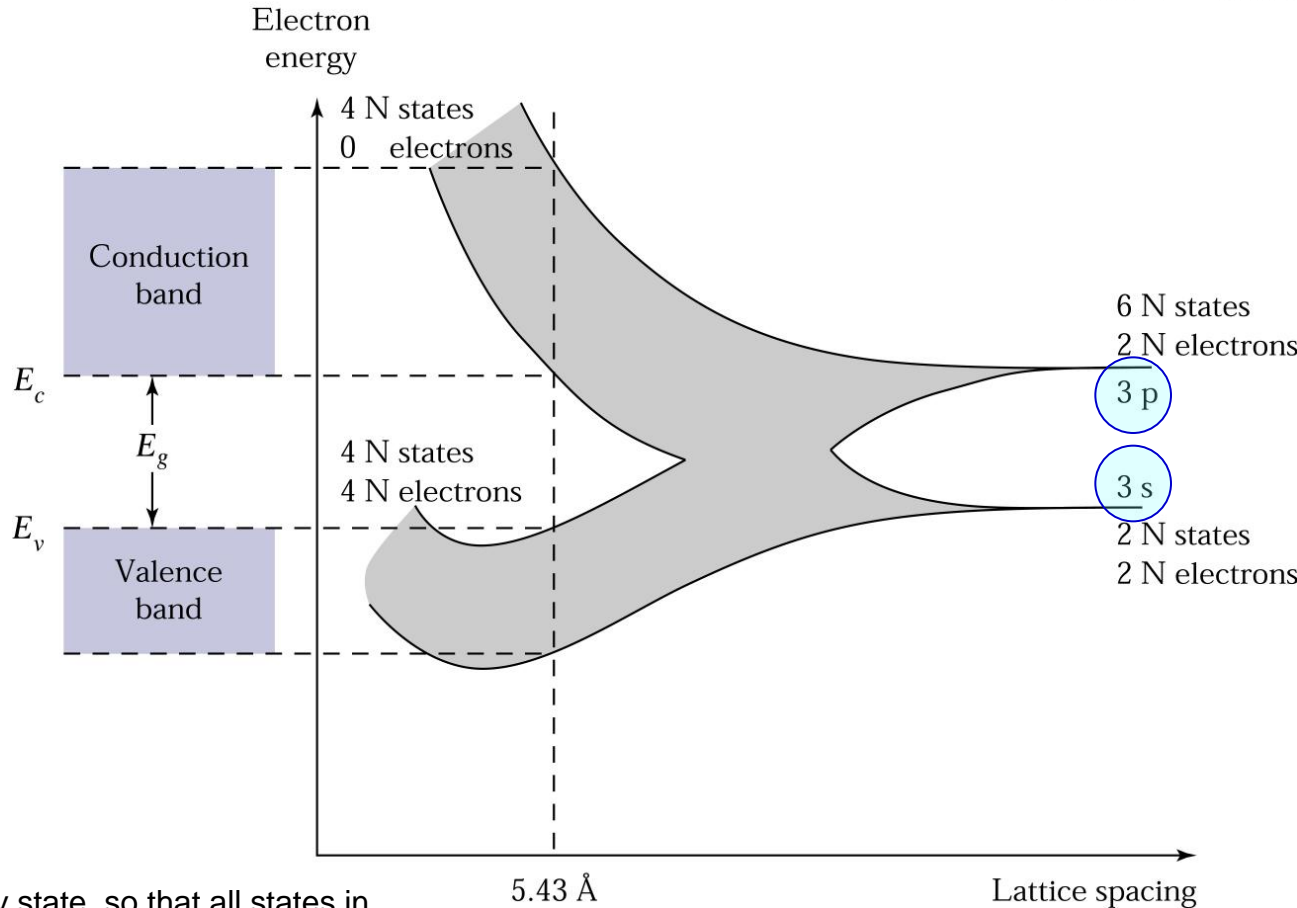
Electrons must occupy  
different energies due to  
Pauli Exclusion principle.





$E_g (\text{Si}) = 1.12 \text{ eV}$

$E_g (\text{GaAs}) = 1.42 \text{ eV}$

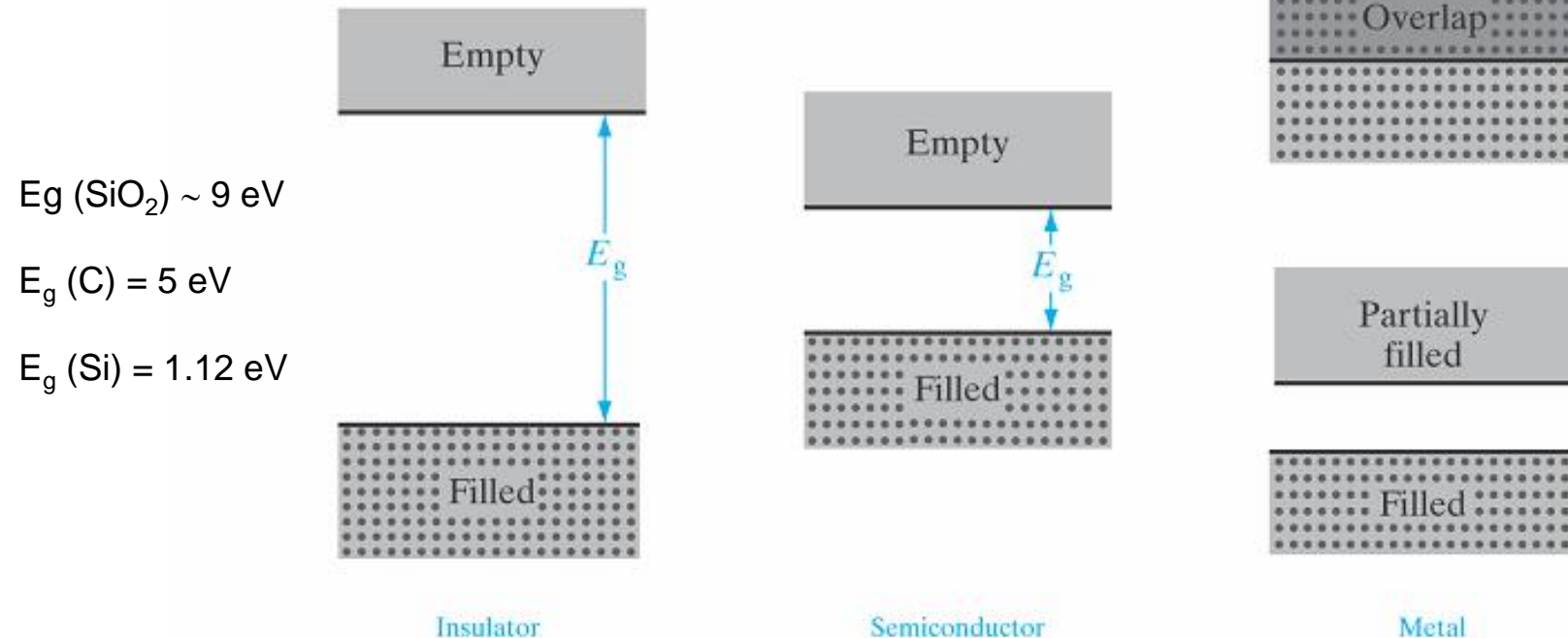


- At 0K, electrons are in the lowest energy state, so that all states in the lower band (valence band) will be full, and all states in the upper band (conduction band) will be empty.
- Thermal excitations in semiconductors above 0K allow electrons to cross the energy gap from the valence to the conduction band. This is essentially the energy required to break a valence bond

*The formation of these energy bands is directly related to the electrical characteristics of the crystal.*

# Insulators, Semiconductors and Metals

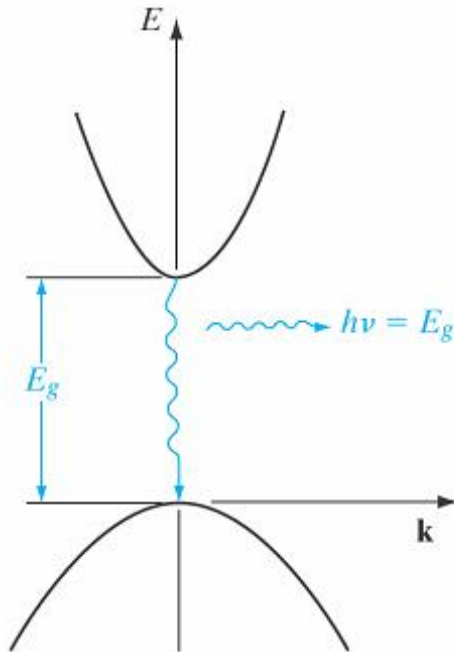
For electrons to experience acceleration in an applied electric field, they must be able to move into new energy states. This implies that *there must be empty states available to the electrons.*



The most important result of application of quantum mechanics to description of electrons in a solid is that the *allowed energy levels of the electrons will be grouped into two bands and they are separated by regions of energy in which the electrons cannot exist.*

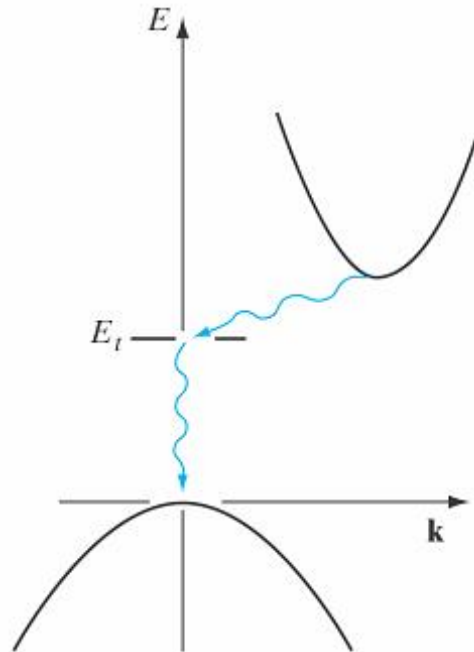
# Direct and Indirect Semiconductors

Since the periodicity of most lattices is different in various directions, the  $(E, \mathbf{k})$  diagram must be plotted for the various crystal directions.



## Direct bandgap (GaAs)

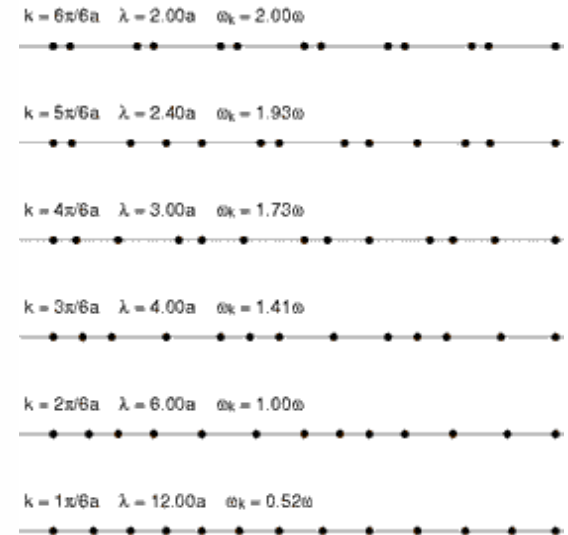
No change in momentum required  
(useful for photonic devices)



## Indirect bandgap (Si)

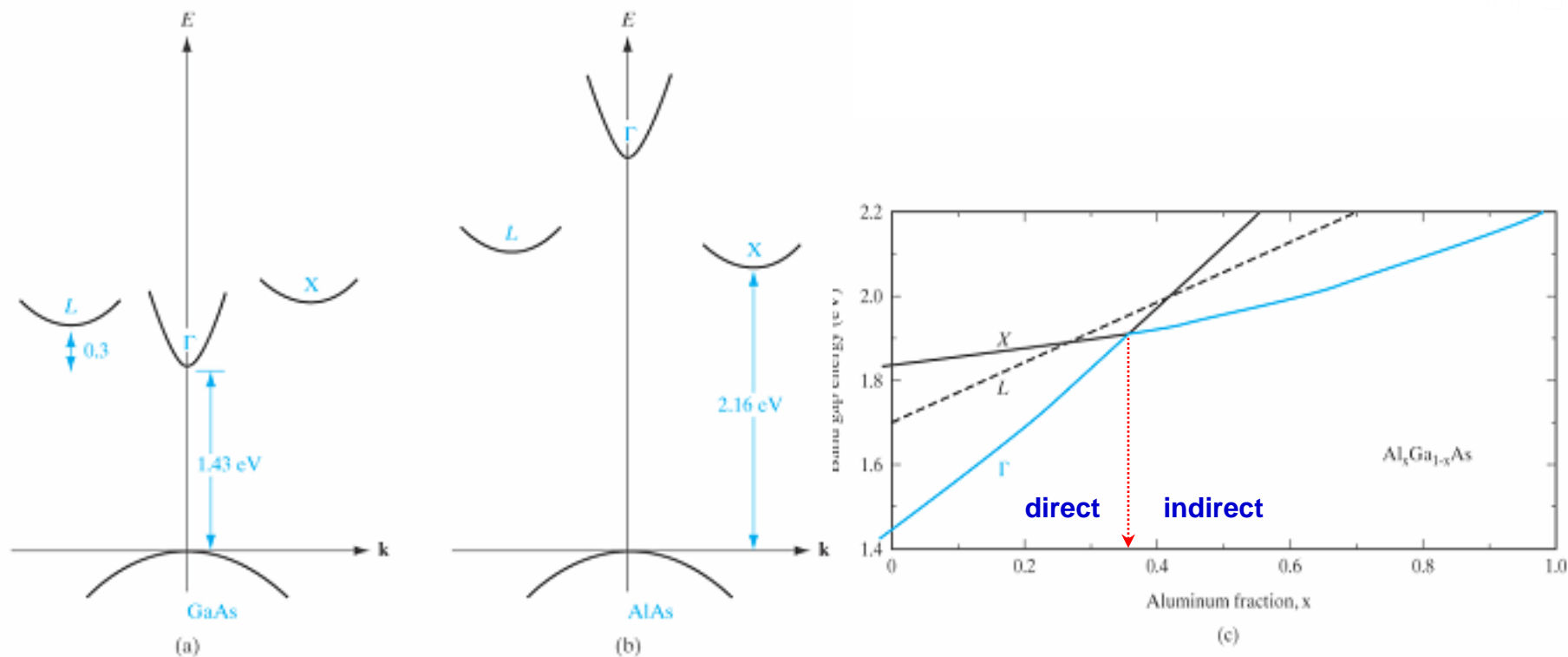
Change in momentum required

*Phonon*: quantized mode of vibration occurring in a rigid crystal lattice (heat)



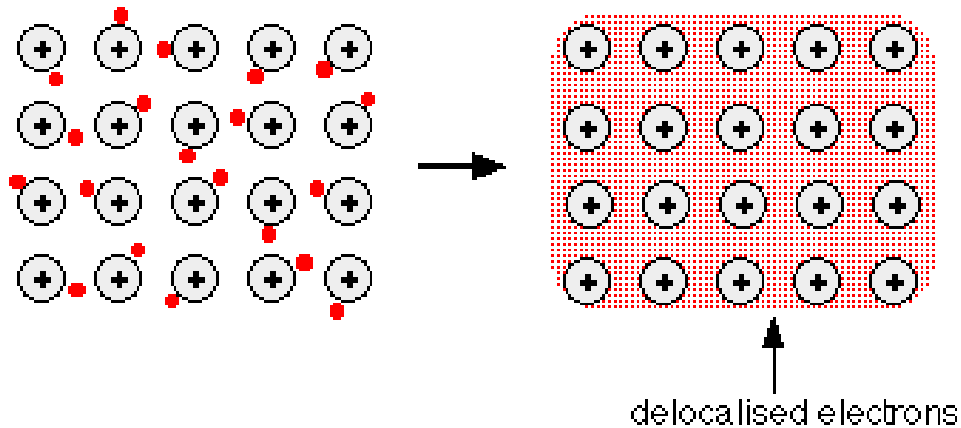
Direct and indirect electron transitions in semiconductors: (a) direct transition with accompanying photon emission; (b) indirect transition via a defect level.

# Variations of Energy Bands with Alloy Composition



# Charge Carriers in Semiconductors

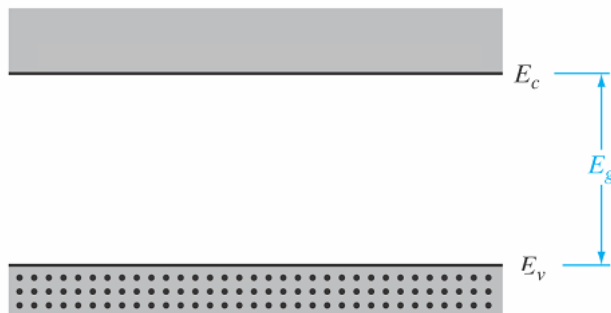
- The mechanism of current conduction is relatively easy to visualize in the case of a metal; the metals atoms are imbedded in a “sea” of relatively free electrons, and these electrons can move as a group under the influence of an electric field.



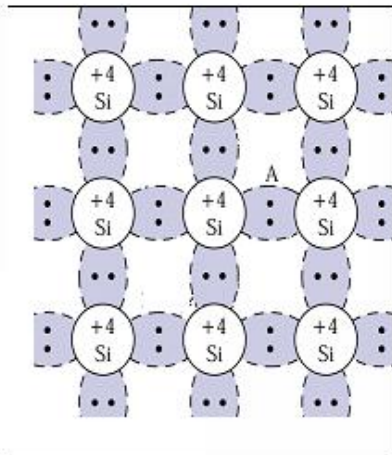
- However, we cannot account for all the electrical properties of semiconductors in this way. Because these properties are strongly *dependent of temperature, doping, electric, optical and magnetic fields*.

# Electrons and Holes

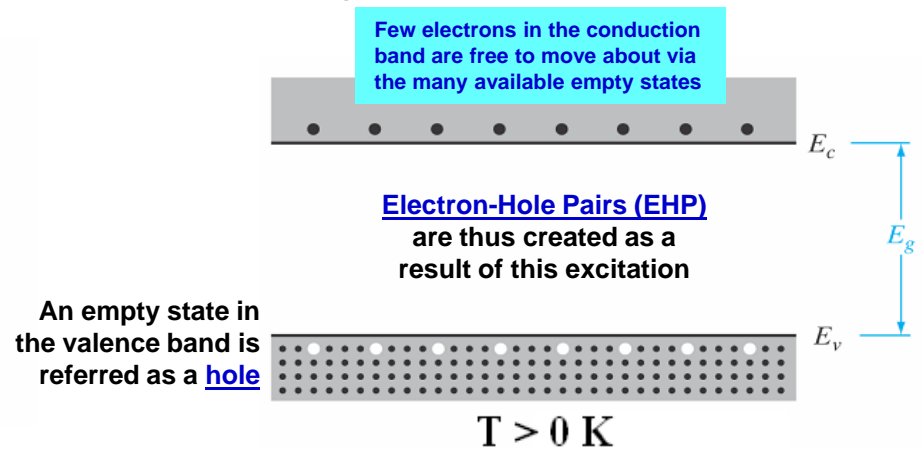
At  $T=0\text{K}$  all bonds remain intact resulting in a highly insulating material.



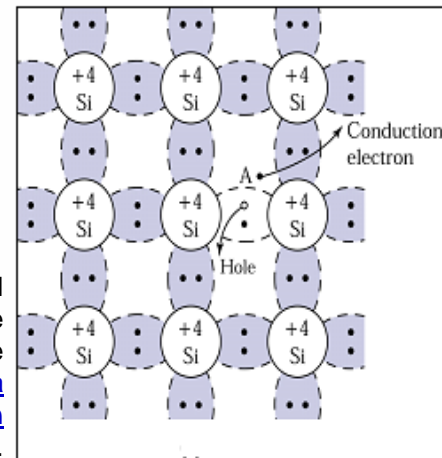
$T = 0\text{ K}$



At  $T>0\text{K}$  thermal excitations may cause bonds to break resulting in free electrons and 'holes': conducting solid.



$T > 0\text{ K}$



In all the following discussions we shall concentrate on the electrons in the conduction band and on the holes in the valence band. The current flow in a semiconductor is done by the motion of these 2 types of charge carriers.

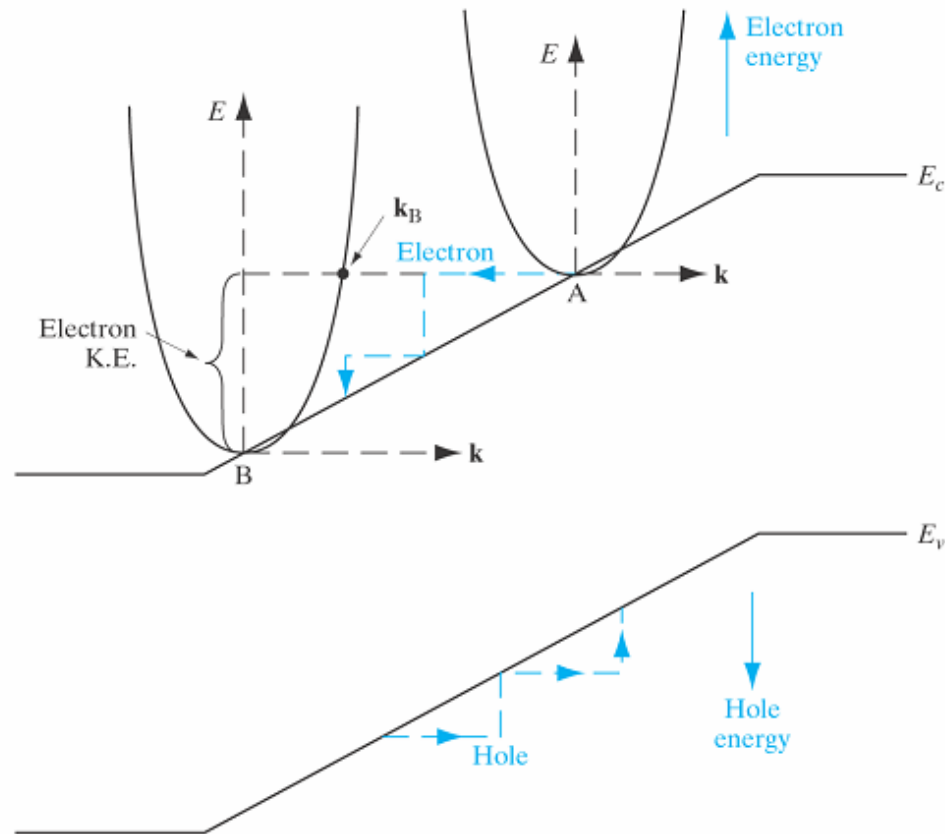
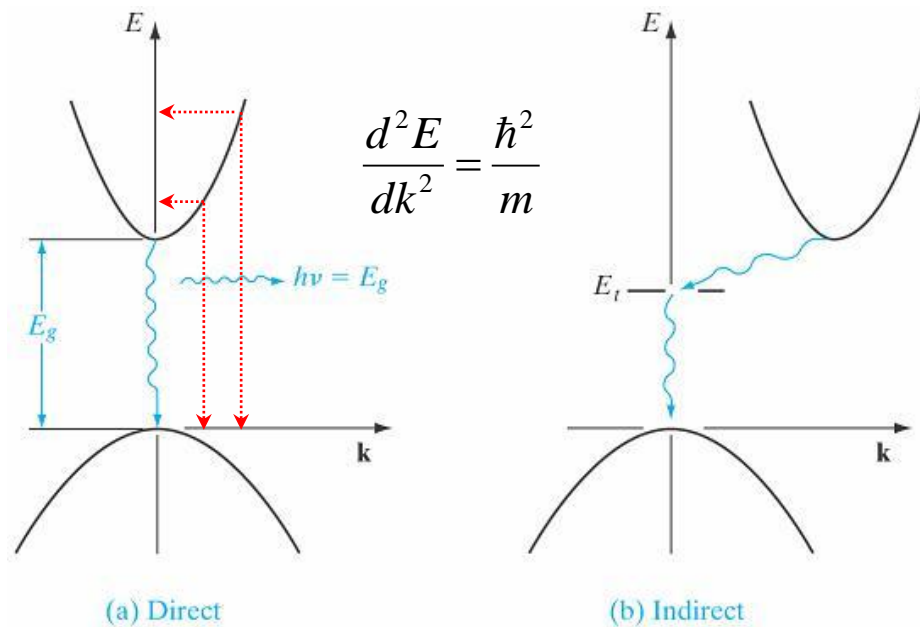


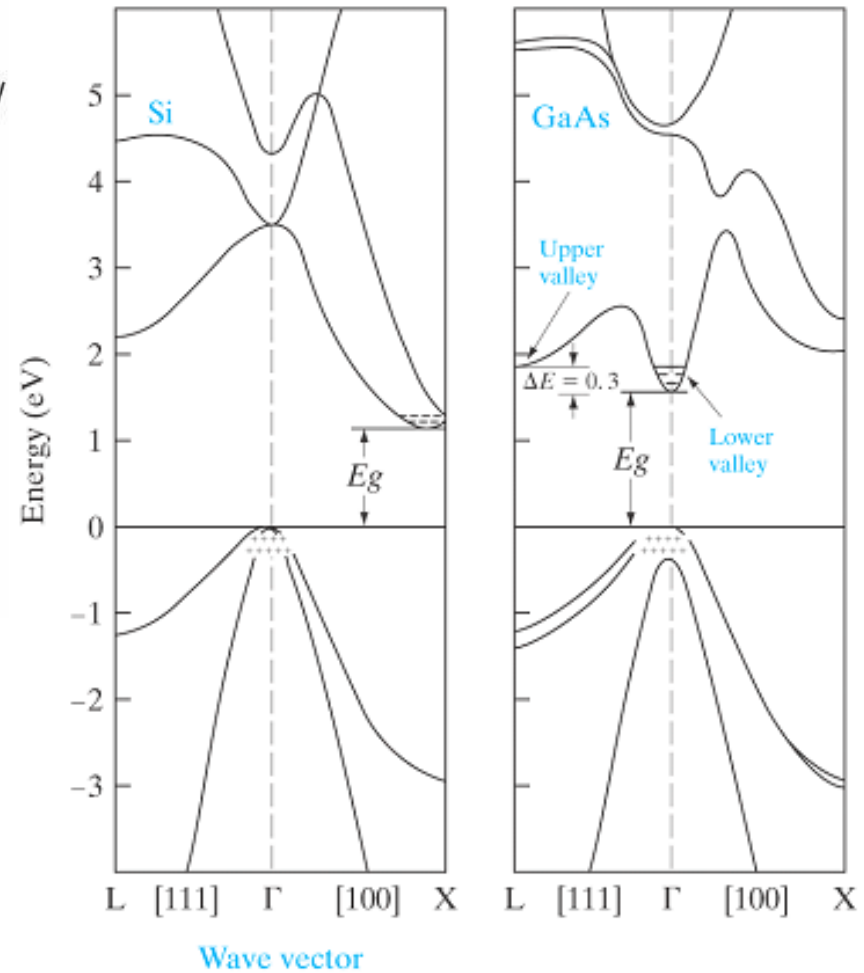
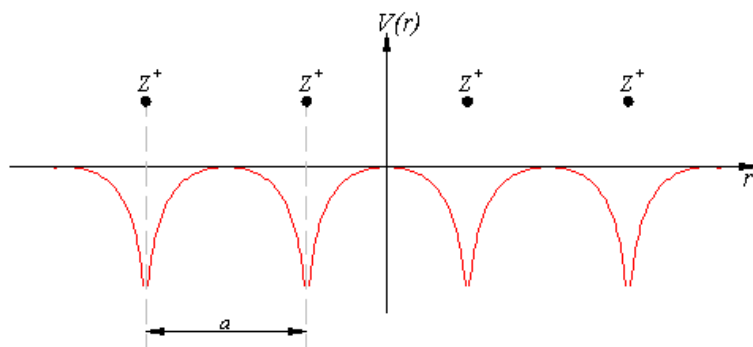
Figure 3.9

Superimposition of the  $(E, k)$  band structure on the  $E$ -versus-position simplified band diagram for a semiconductor in an electric field. Electron energies increase going up, while hole energies increase going down. Similarly, electron and hole wave vectors point in opposite directions and these charge carriers move opposite to each other, as shown.

# Ideal and realistic band structures in semiconductors



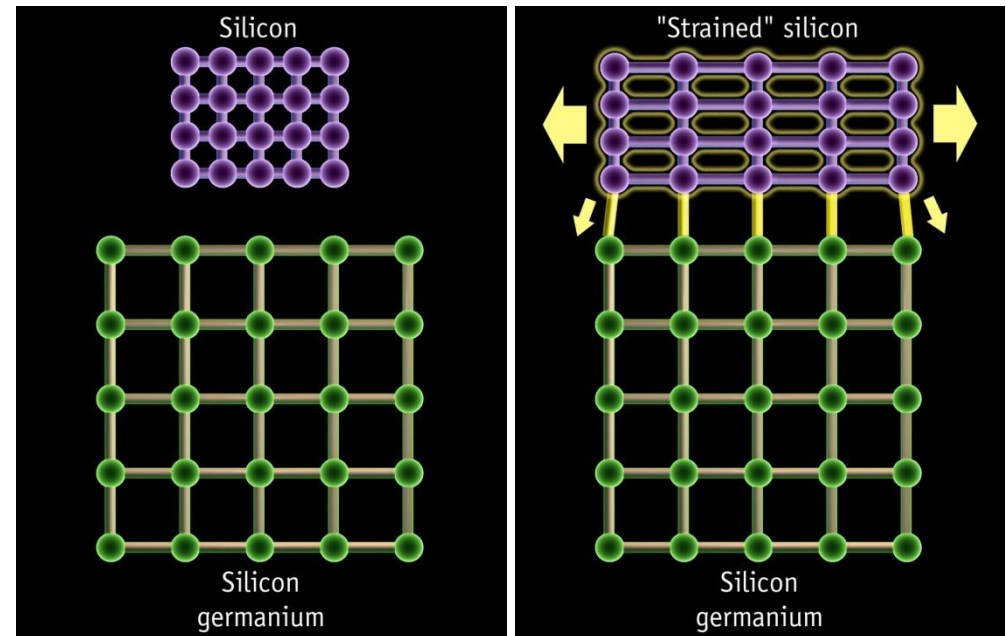
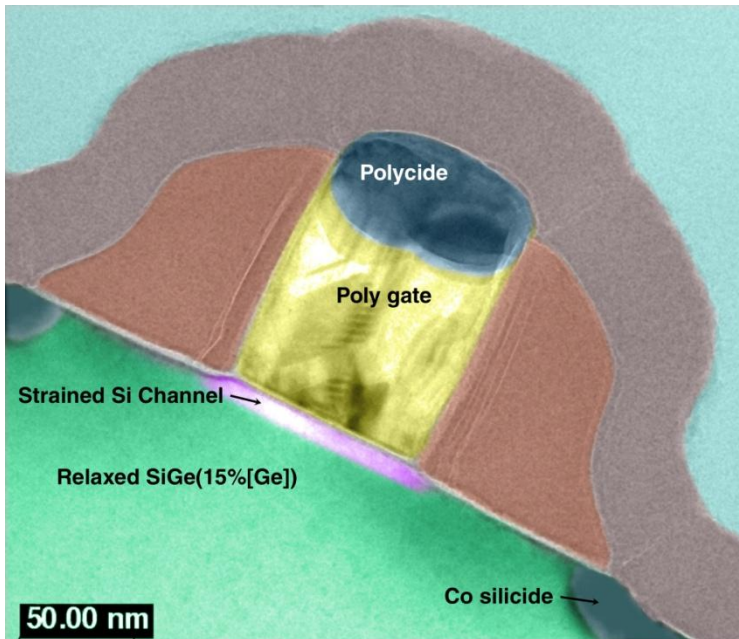
The electrons in a crystal are not completely free, but instead interact with the periodic potential of the lattice.





# Strained-silicon based MOSFETs

Strained silicon is a layer of silicon in which the silicon atoms are stretched beyond their normal interatomic distance. This can be accomplished by putting the layer of silicon over a substrate of silicon germanium (SiGe), where the atoms are farther apart. The linking of the silicon atoms with the SiGe substrate become stretched - thereby leading to strained silicon.



The silicon is "stretched out" because of the natural tendency for atoms inside compounds to align with one another. When silicon is deposited on top of a substrate with atoms spaced farther apart, the atoms in silicon stretch to line up with the atoms beneath, stretching -- or "straining" -- the silicon **... without having to shrink the size of transistors.**